

[illegible][illegible][illegible][illegible]

Model Name:GA-A55M-DS2

Component value change history

Version: 1.01


P-Code: U98126-0

[illegible]

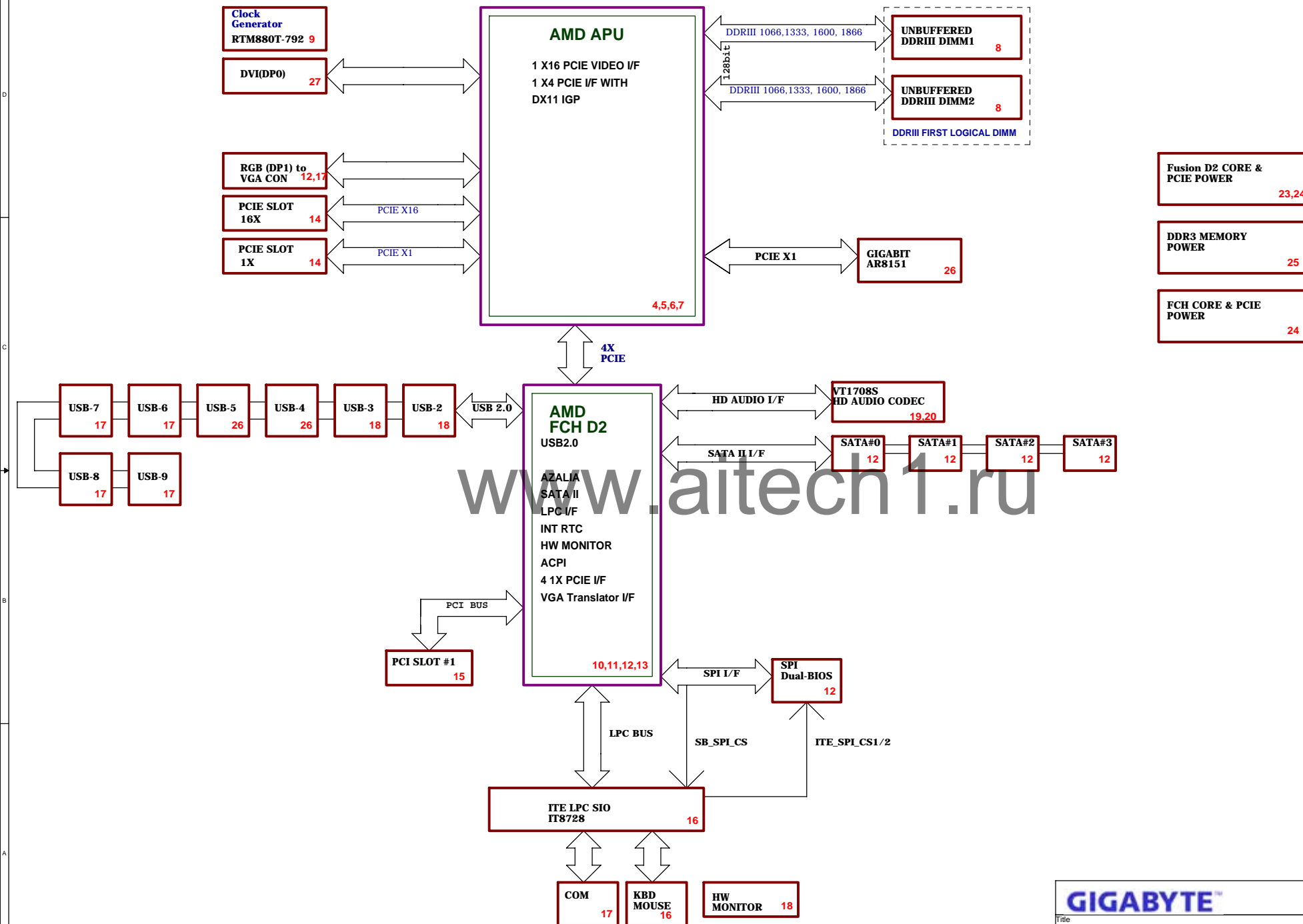
Circuit or PCB layout change for next version

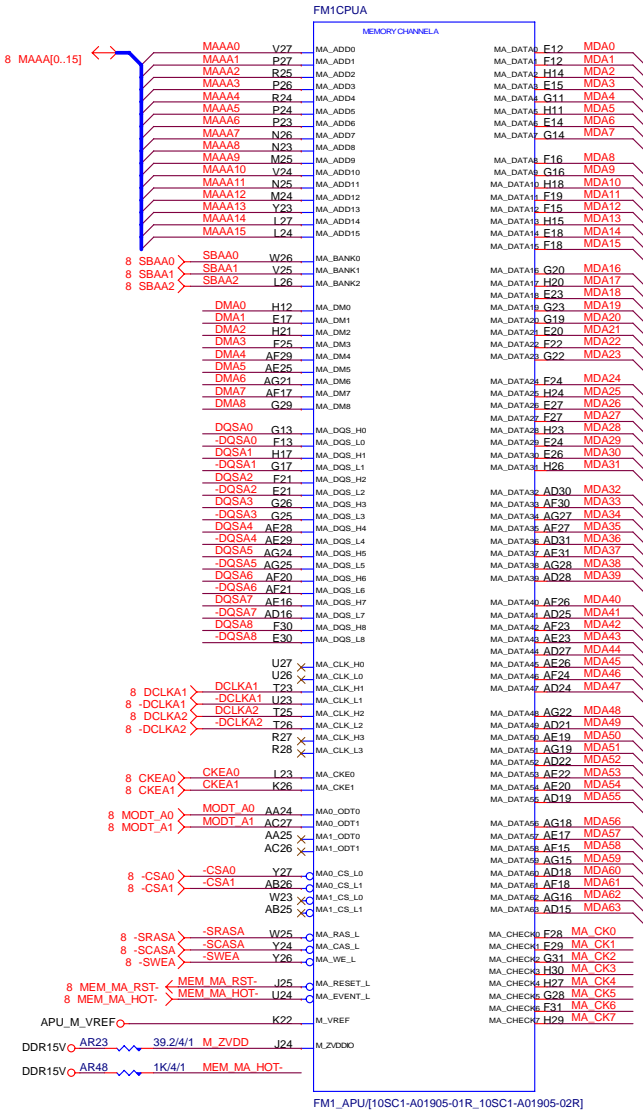
[illegible]

www.aitech1.ru

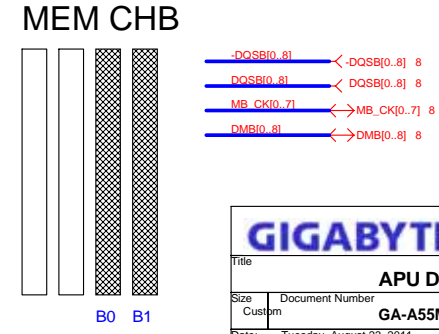
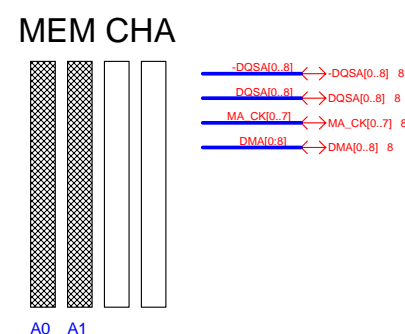
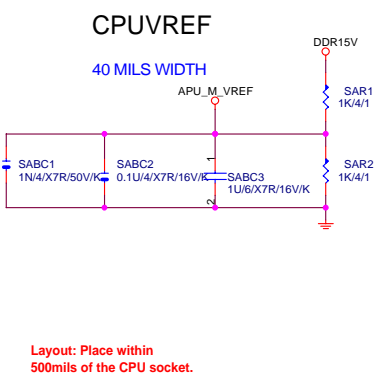
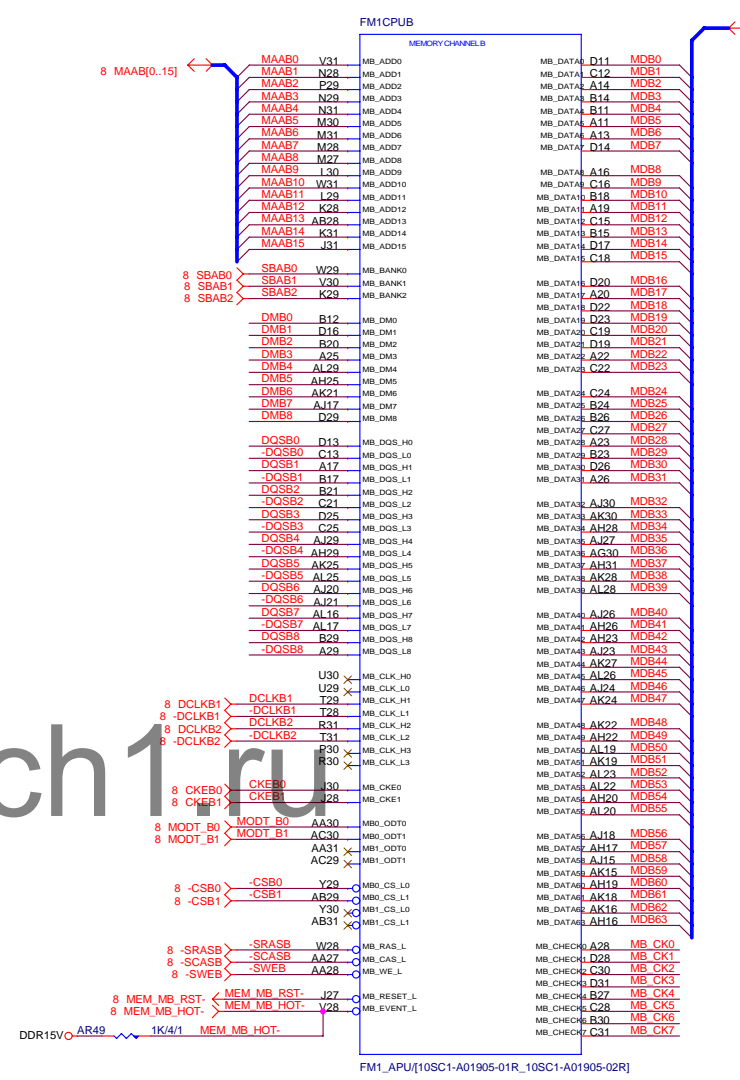
				
Title				
BOM & PCB HISTORY				
Size	Document Number			Rev
Custom	GA-A55M-DS2			1.01
Date:	Tuesday, August 23, 2011	Sheet	2	of 27

AMD Fusion Hudson CUSTOMER DESKTOP REFERENCE DESIGN





www.aitech1.ru



GIGABYTE

Title: APU DDRIII MEMORY

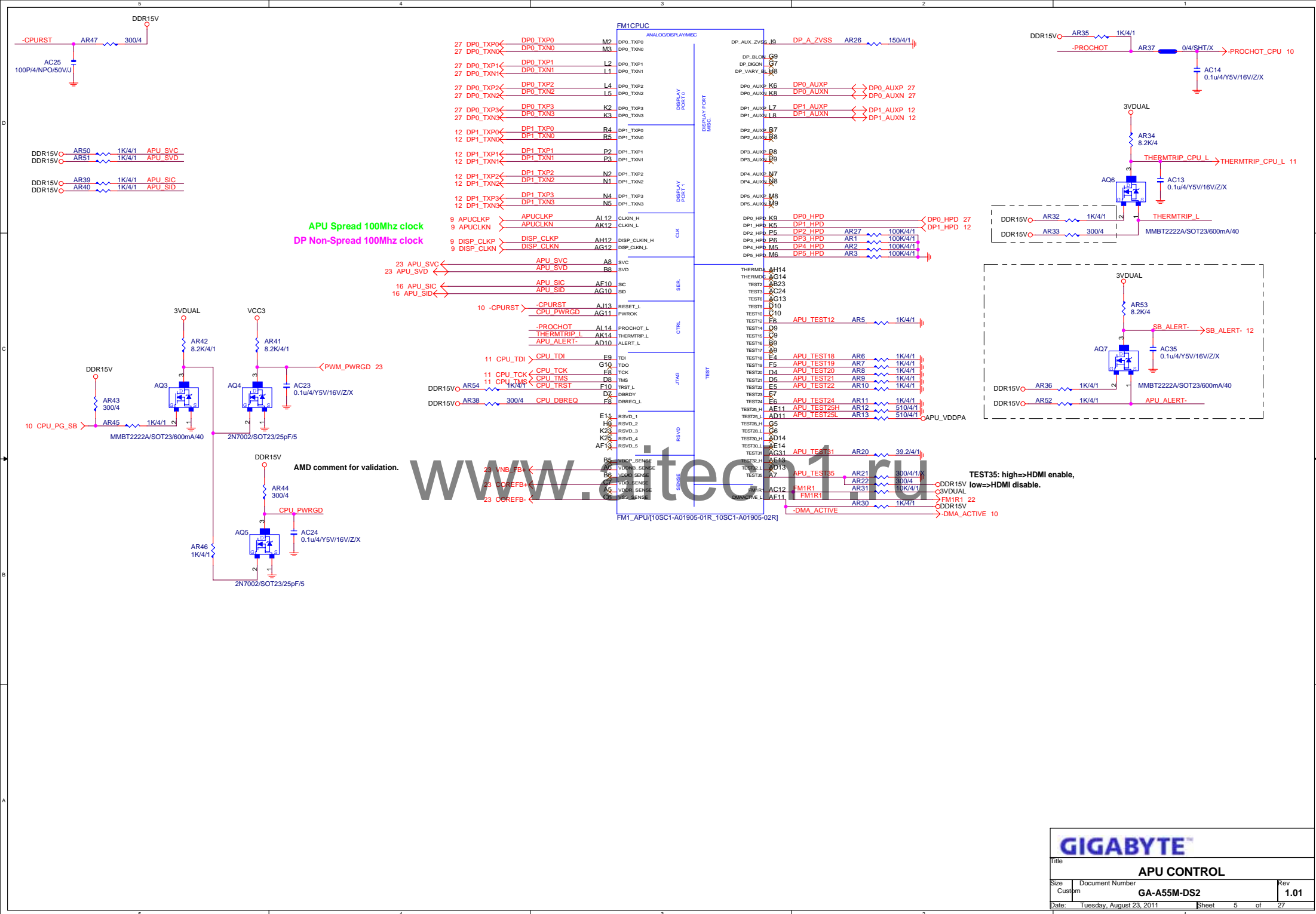
Size: Custom

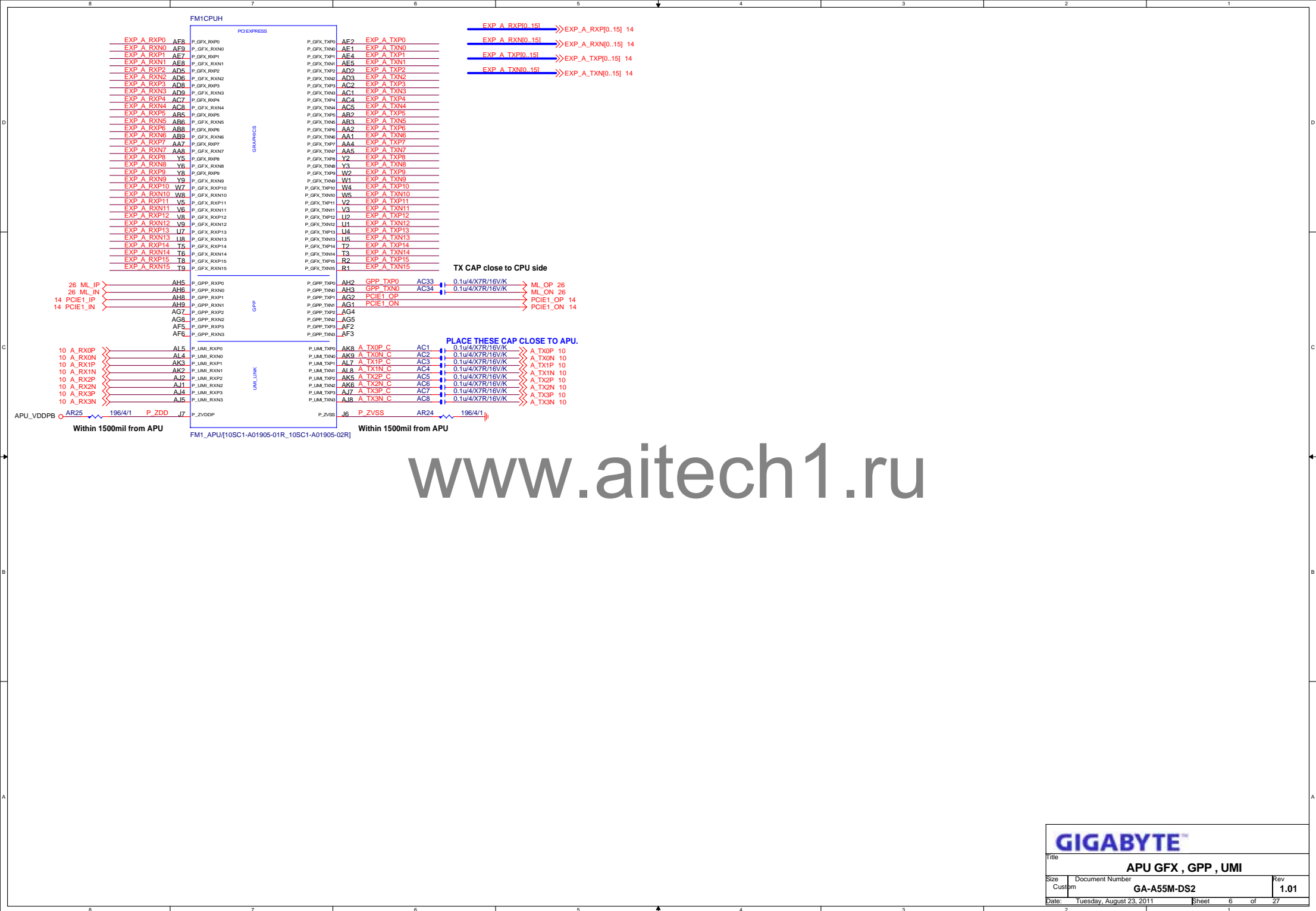
Document Number: GA-A55M-DS2

Date: Tuesday, August 23, 2011

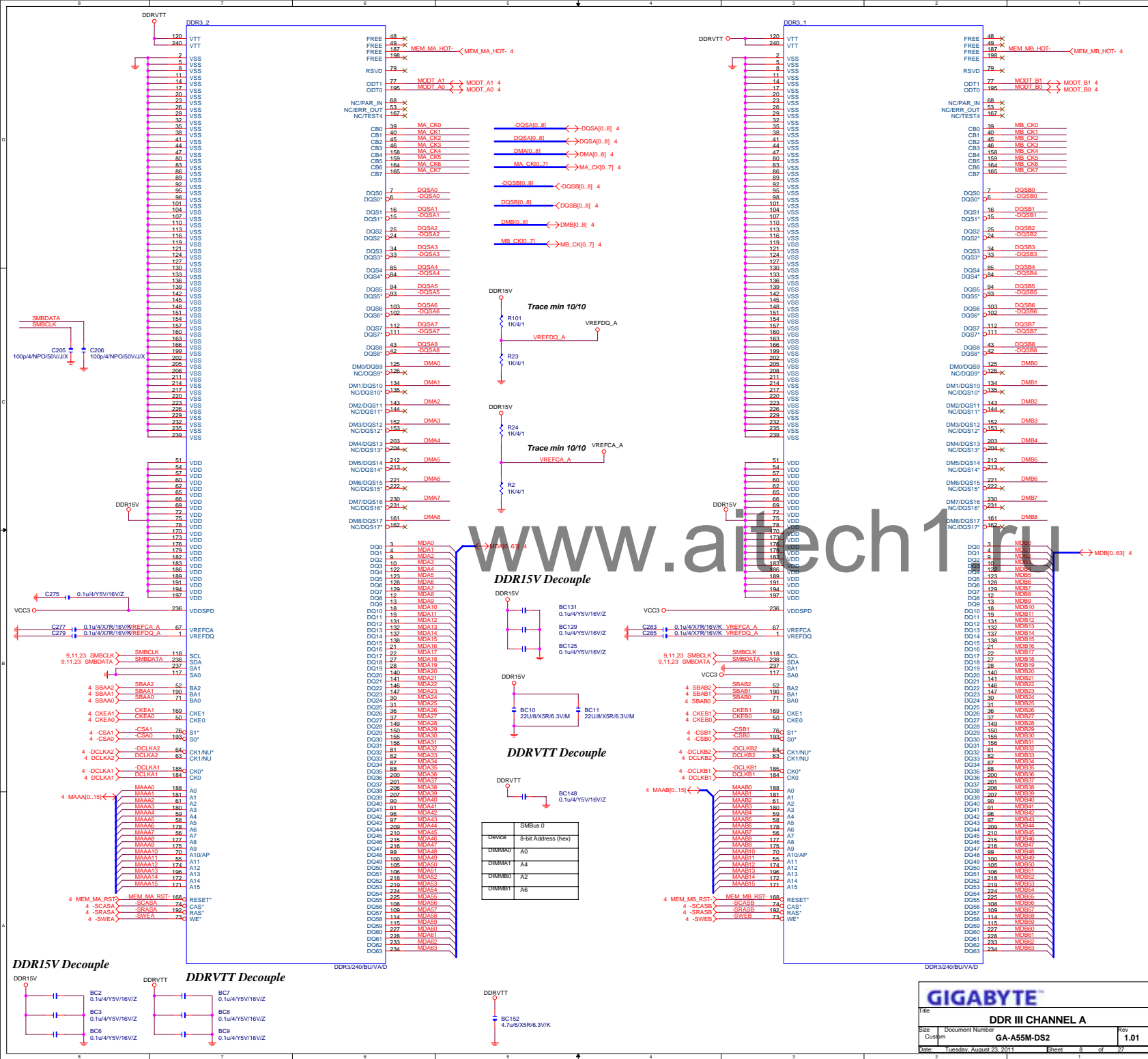
Sheet: 4 of 27

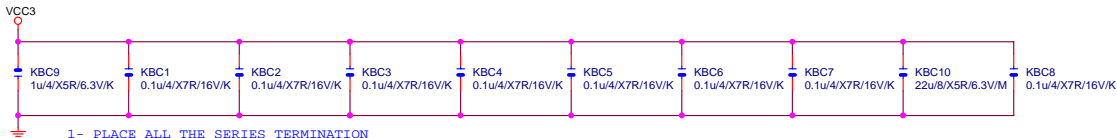
Rev: 1.01











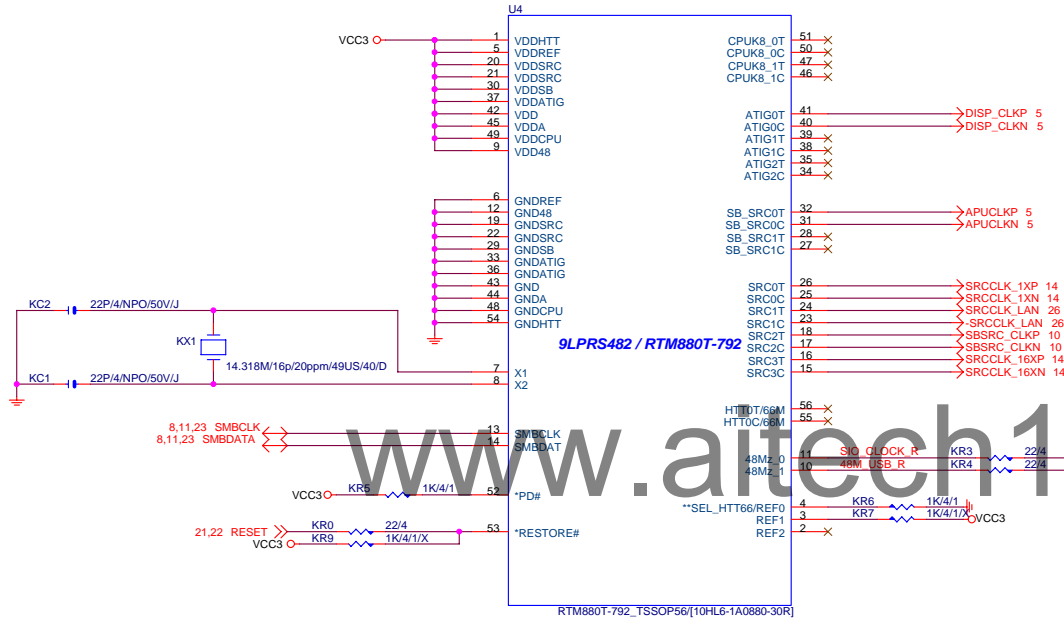
- 1- PLACE ALL THE SERIES TERMINATION RESISTORS AS CLOSE TO U800 AS POSSIBLE
- 2- ROUTE ALL SRCCLKTx AND SRCCLKCx AS DIFFERENT PAIR RULE
- 3- PUT DECOUPLING CAPS CLOSE TO U800 POWER PIN



NB CLOCK INPUT TABLE

NB CLOCKS	RS740	RX780	RS780	
HT_REFCLKP	66M SE(SE)	100M DIFF	100M DIFF	
HT_REFCLKN	NC	100M DIFF	100M DIFF	
REFCLK_P	14M SE (3.3V)	14M SE (1.8V)	14M SE (1.1V)	100M DIFF
REFCLK_N	NC	NC	vref	100M DIFF
GFX_REFCLK*	100M DIFF	100M DIFF	100M DIFF	100M DIFF
GPP_REFCLK	NC	100M DIFF	100M DIFF(OUT)	
GPPSB_REFCLK	100M DIFF	100M DIFF	100M DIFF	

* the GFX_REFCLK input is required for all cases



Clock chip has internal serial terminations for differential pairs, external resistors are reserved for debug purpose.

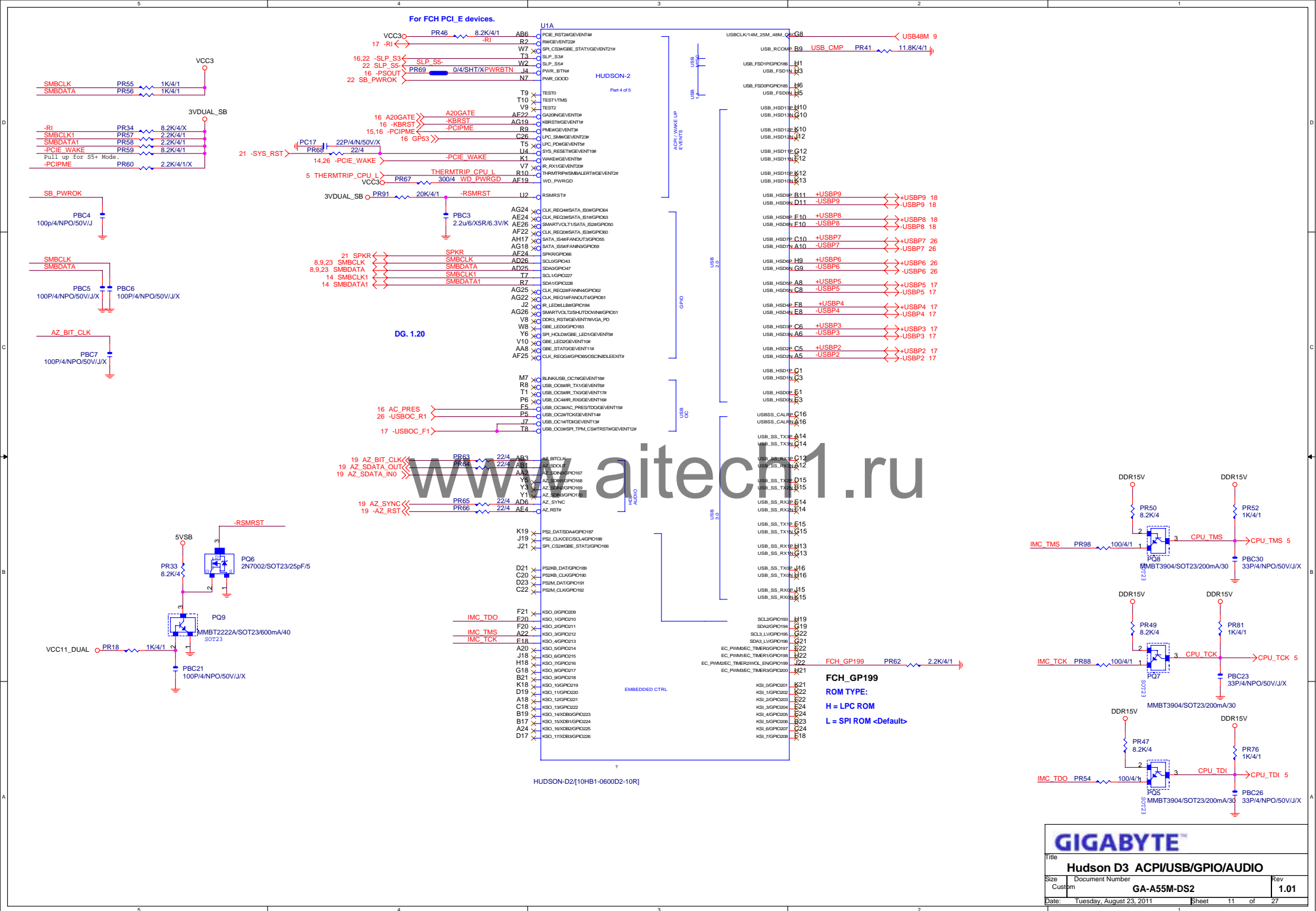
watch dog --
RESTORE# 接 RESET

REF0/SEL_HTT66	HTT CLOCK
0	100.00 DIFFERENTIAL
1	66.66 SINGLE END

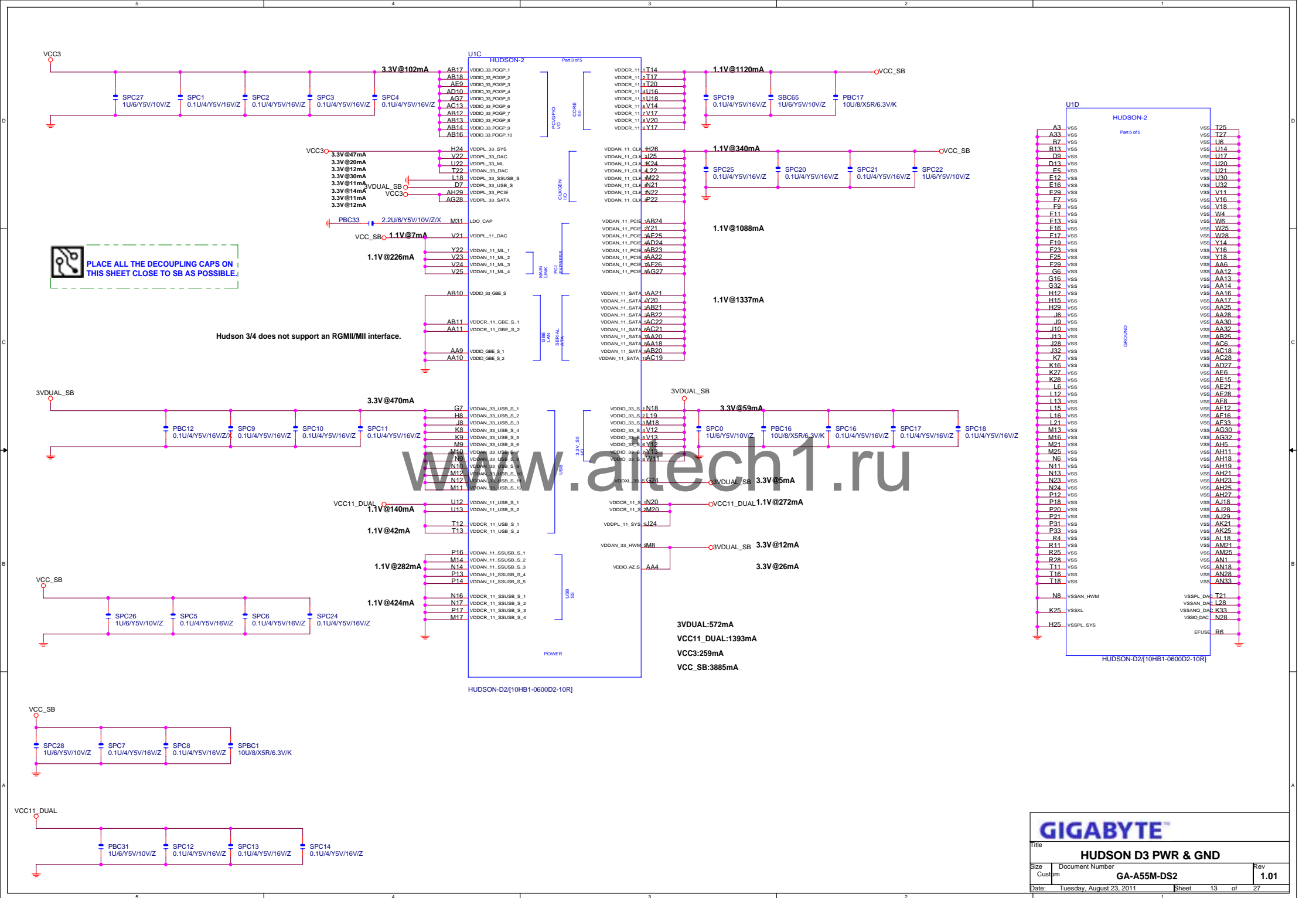
REF1/SEL_SATA	SRC6/SATA
0	100.00 DIFFERENTIAL SPREADING SRC CLOCK
1	100.00 NON-SPREADING DIFFERENTIAL SATA CLOCK

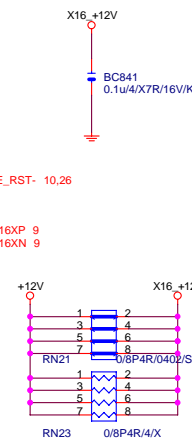
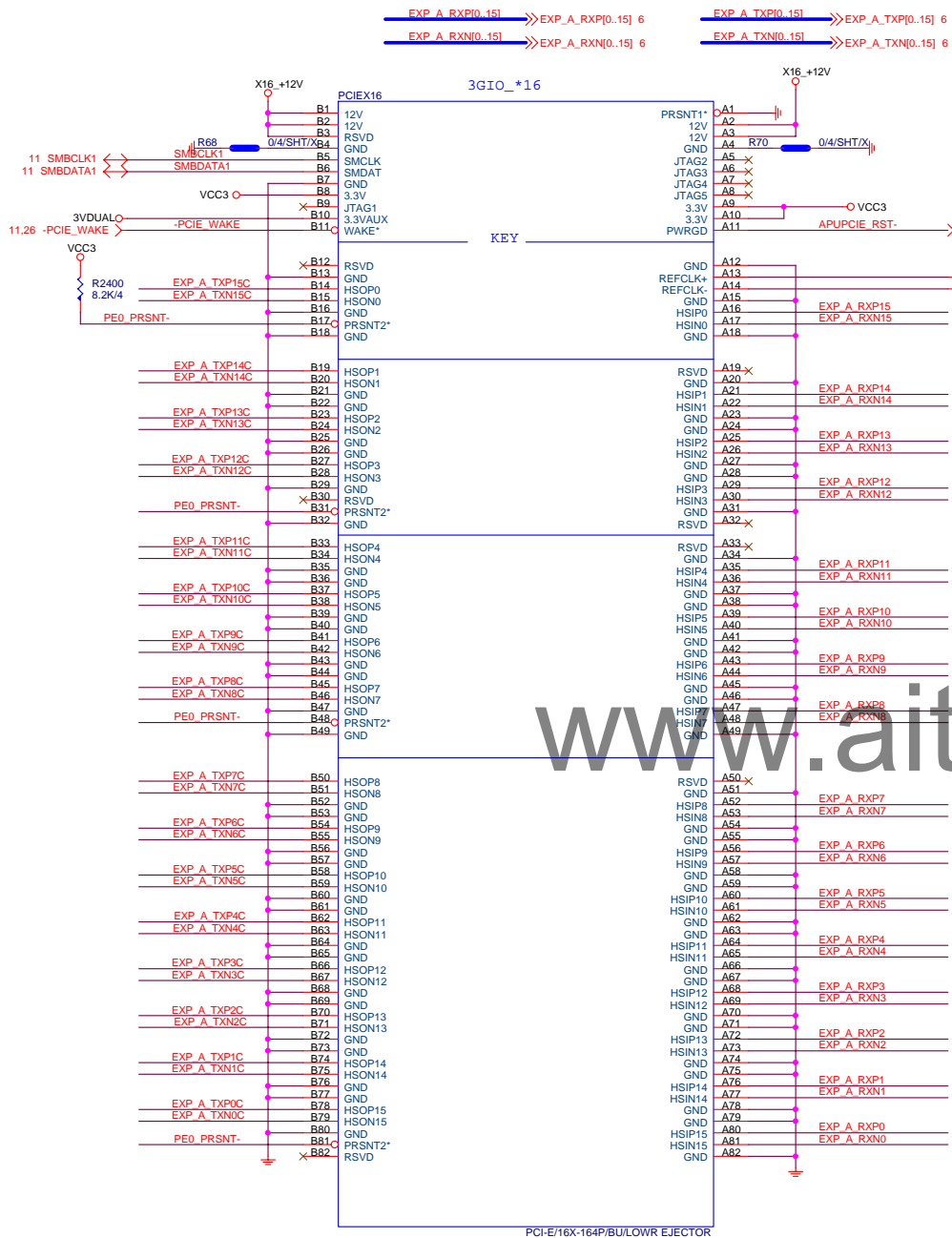
	OSC_14M_NB
RS740	3.3V 33R serial
RX780	1.8V 82.5R/130R
RS780	1.1V 158R/90.9R
(Single-ended)	

GIGABYTE™			
Title			
RTM880T-792			
Size	Document Number	Rev	
Custom	GA-A55M-DS2	1.01	
Date:	Tuesday, August 23, 2011	Sheet	9 of 27

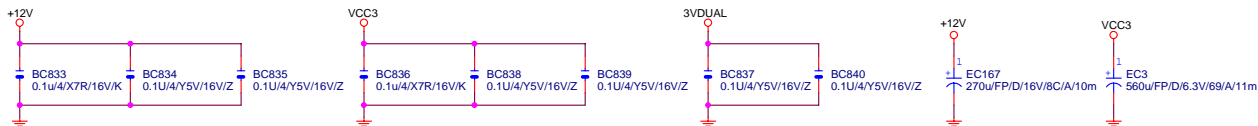
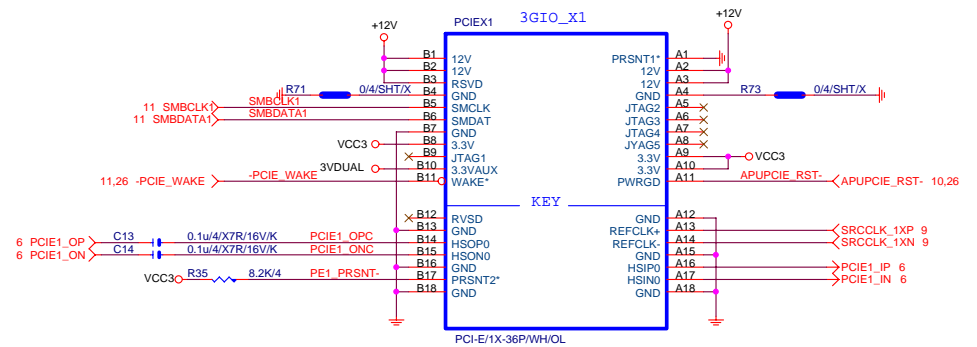
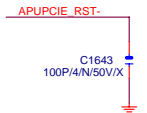






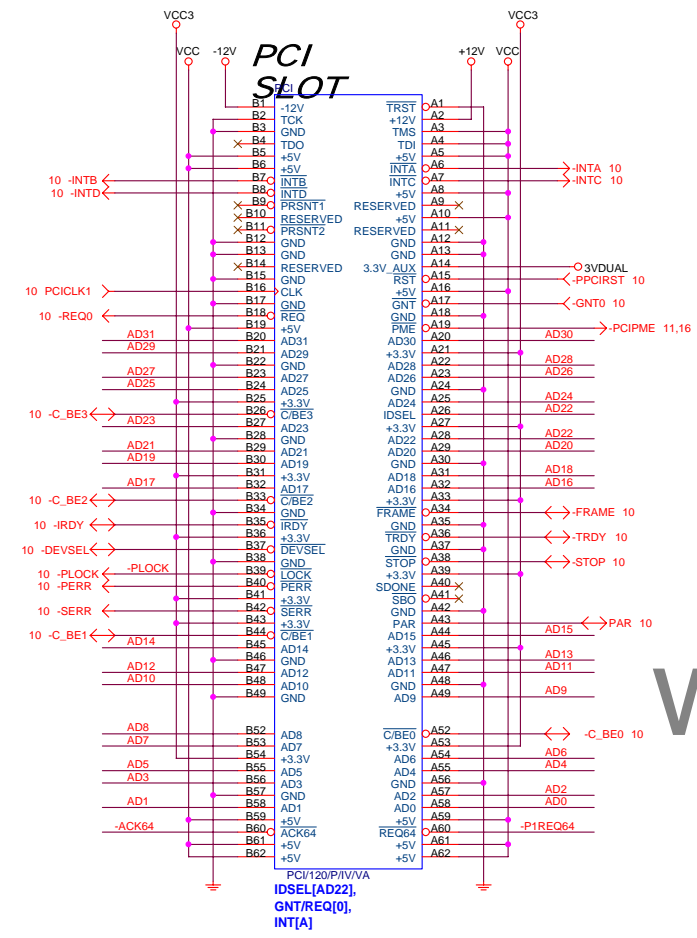


EXP A TXP0	C1644	0.1u/4/X7R/16V/K	EXP A TXP0C
EXP A TXN0	C1645	0.1u/4/X7R/16V/K	EXP A TXN0C
EXP A TXP1	C1646	0.1u/4/X7R/16V/K	EXP A TXP1C
EXP A TXN1	C1647	0.1u/4/X7R/16V/K	EXP A TXN1C
EXP A TXP2	C1648	0.1u/4/X7R/16V/K	EXP A TXP2C
EXP A TXN2	C1649	0.1u/4/X7R/16V/K	EXP A TXN2C
EXP A TXP3	C1650	0.1u/4/X7R/16V/K	EXP A TXP3C
EXP A TXN3	C1651	0.1u/4/X7R/16V/K	EXP A TXN3C
EXP A TXP4	C1652	0.1u/4/X7R/16V/K	EXP A TXP4C
EXP A TXN4	C1653	0.1u/4/X7R/16V/K	EXP A TXN4C
EXP A TXP5	C1654	0.1u/4/X7R/16V/K	EXP A TXP5C
EXP A TXN5	C1655	0.1u/4/X7R/16V/K	EXP A TXN5C
EXP A TXP6	C1656	0.1u/4/X7R/16V/K	EXP A TXP6C
EXP A TXN6	C1657	0.1u/4/X7R/16V/K	EXP A TXN6C
EXP A TXP7	C1658	0.1u/4/X7R/16V/K	EXP A TXP7C
EXP A TXN7	C1659	0.1u/4/X7R/16V/K	EXP A TXN7C
EXP A TXP8	C1660	0.1u/4/X7R/16V/K	EXP A TXP8C
EXP A TXN8	C1661	0.1u/4/X7R/16V/K	EXP A TXN8C
EXP A TXP9	C1662	0.1u/4/X7R/16V/K	EXP A TXP9C
EXP A TXN9	C1663	0.1u/4/X7R/16V/K	EXP A TXN9C
EXP A TXP10	C1664	0.1u/4/X7R/16V/K	EXP A TXP10C
EXP A TXN10	C1665	0.1u/4/X7R/16V/K	EXP A TXN10C
EXP A TXP11	C1666	0.1u/4/X7R/16V/K	EXP A TXP11C
EXP A TXN11	C1667	0.1u/4/X7R/16V/K	EXP A TXN11C
EXP A TXP12	C1668	0.1u/4/X7R/16V/K	EXP A TXP12C
EXP A TXN12	C1669	0.1u/4/X7R/16V/K	EXP A TXN12C
EXP A TXP13	C1670	0.1u/4/X7R/16V/K	EXP A TXP13C
EXP A TXN13	C1671	0.1u/4/X7R/16V/K	EXP A TXN13C
EXP A TXP14	C1672	0.1u/4/X7R/16V/K	EXP A TXP14C
EXP A TXN14	C1673	0.1u/4/X7R/16V/K	EXP A TXN14C
EXP A TXP15	C1674	0.1u/4/X7R/16V/K	EXP A TXP15C
EXP A TXN15	C1675	0.1u/4/X7R/16V/K	EXP A TXN15C



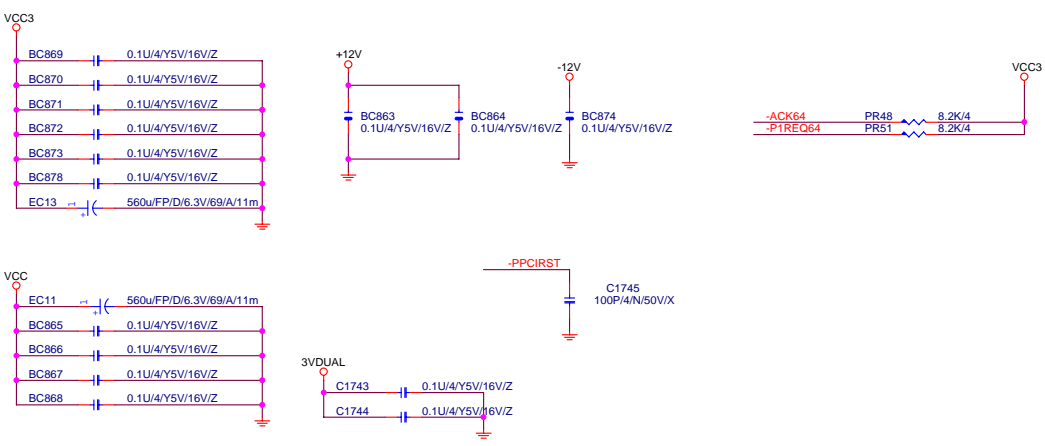
PCI SLOT 1,2

10 AD[0..31] <-> AD[0..31]



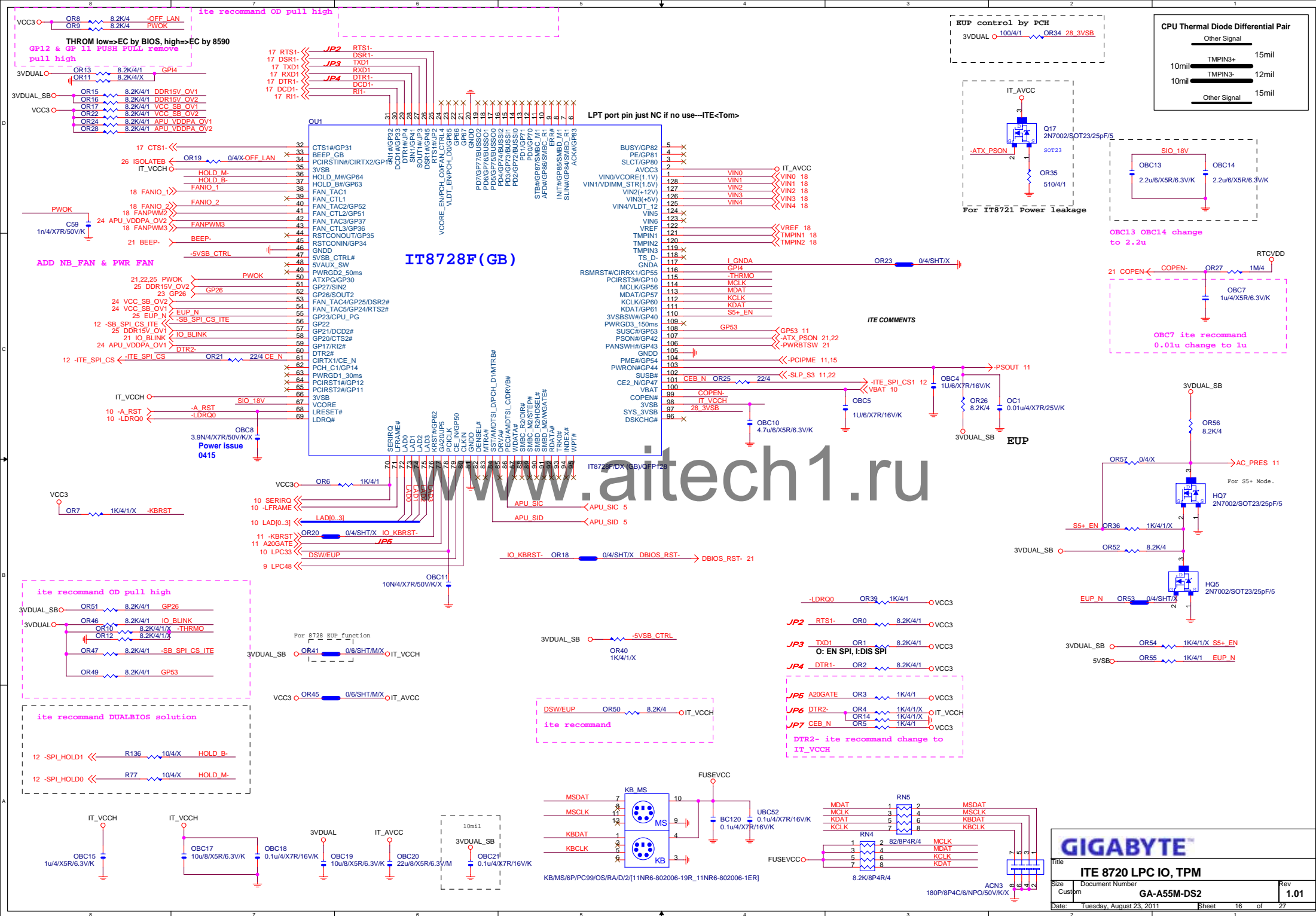
PCI/120/P/IV/VA
IDSEL[AD22],
GNT/REQ[0],
INT[A]

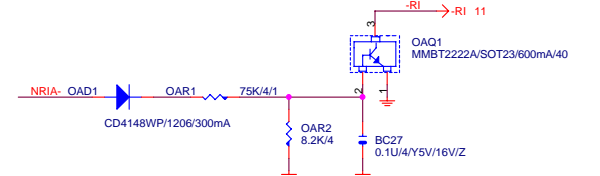
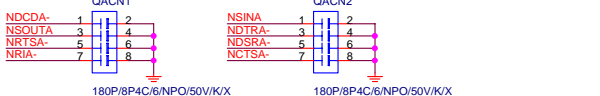
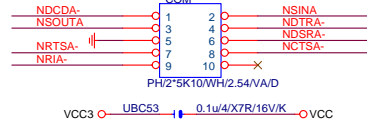
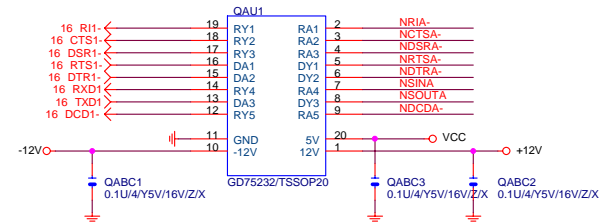
www.aitech1.ru



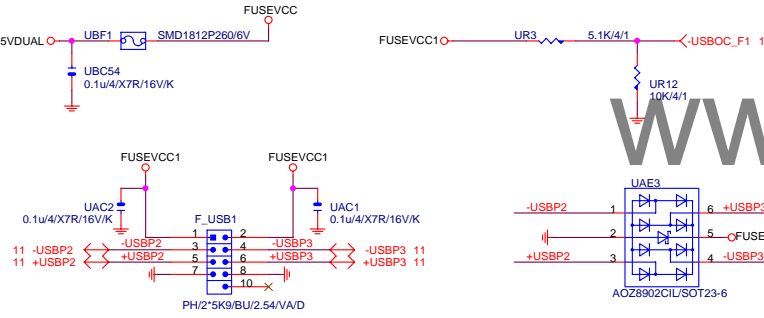
Design Guide:101
Design Guide:101
Design Guide:101
Design Guide:101

GIGABYTE™			
Title			
PCI SLOT			
Size	Document Number	Rev	
Custom	GA-A55M-DS2	1.01	
Date:	Tuesday, August 23, 2011	Sheet	15 of 27

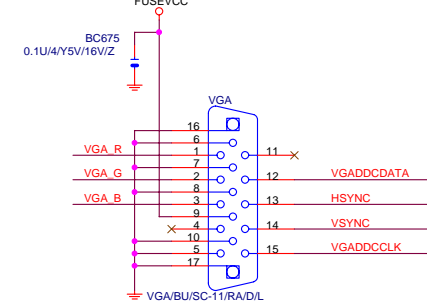
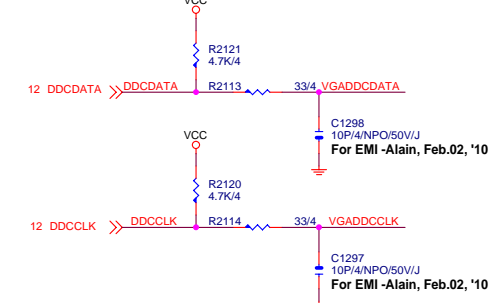
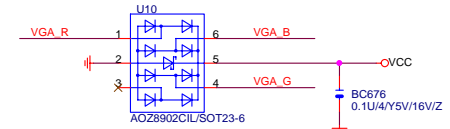
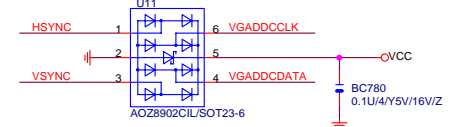
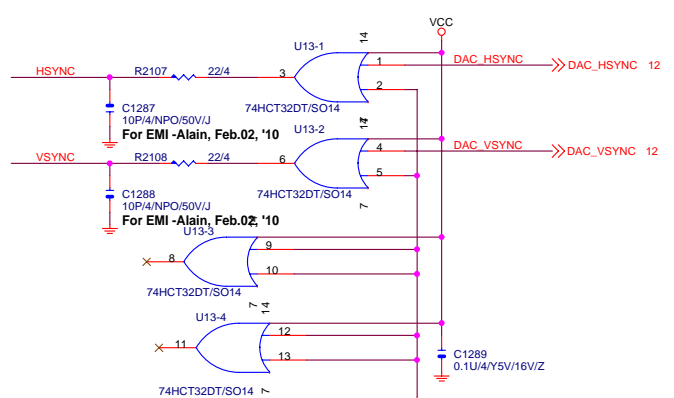
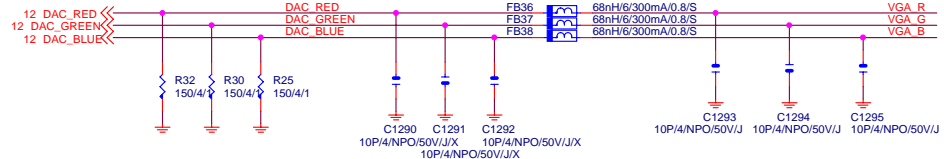
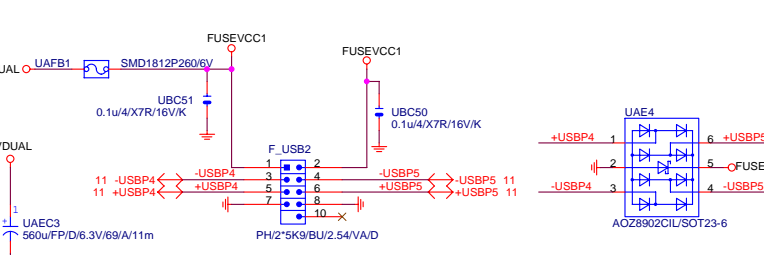




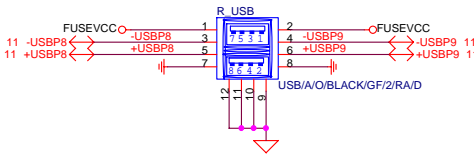
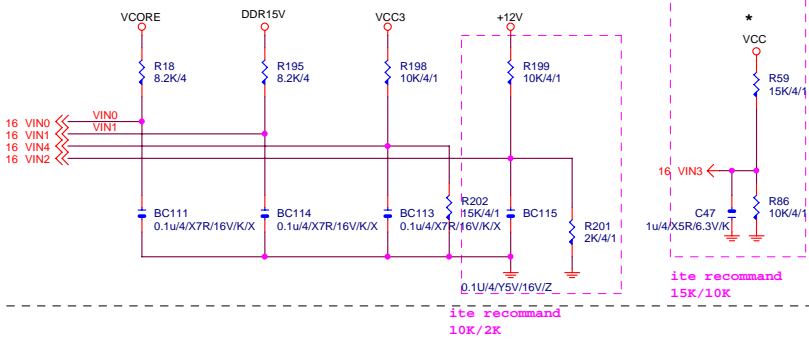
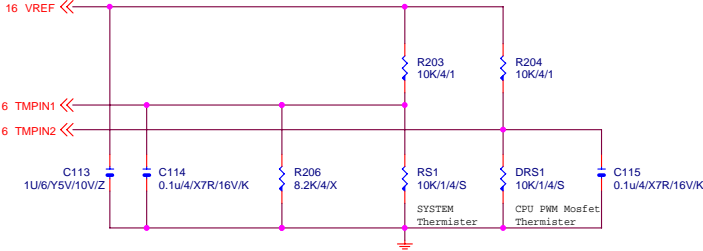
FRONT SIDE USB1



FRONT SIDE USB2



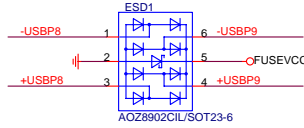
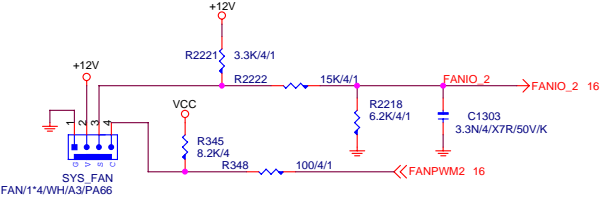
Hardware Monitor circuits



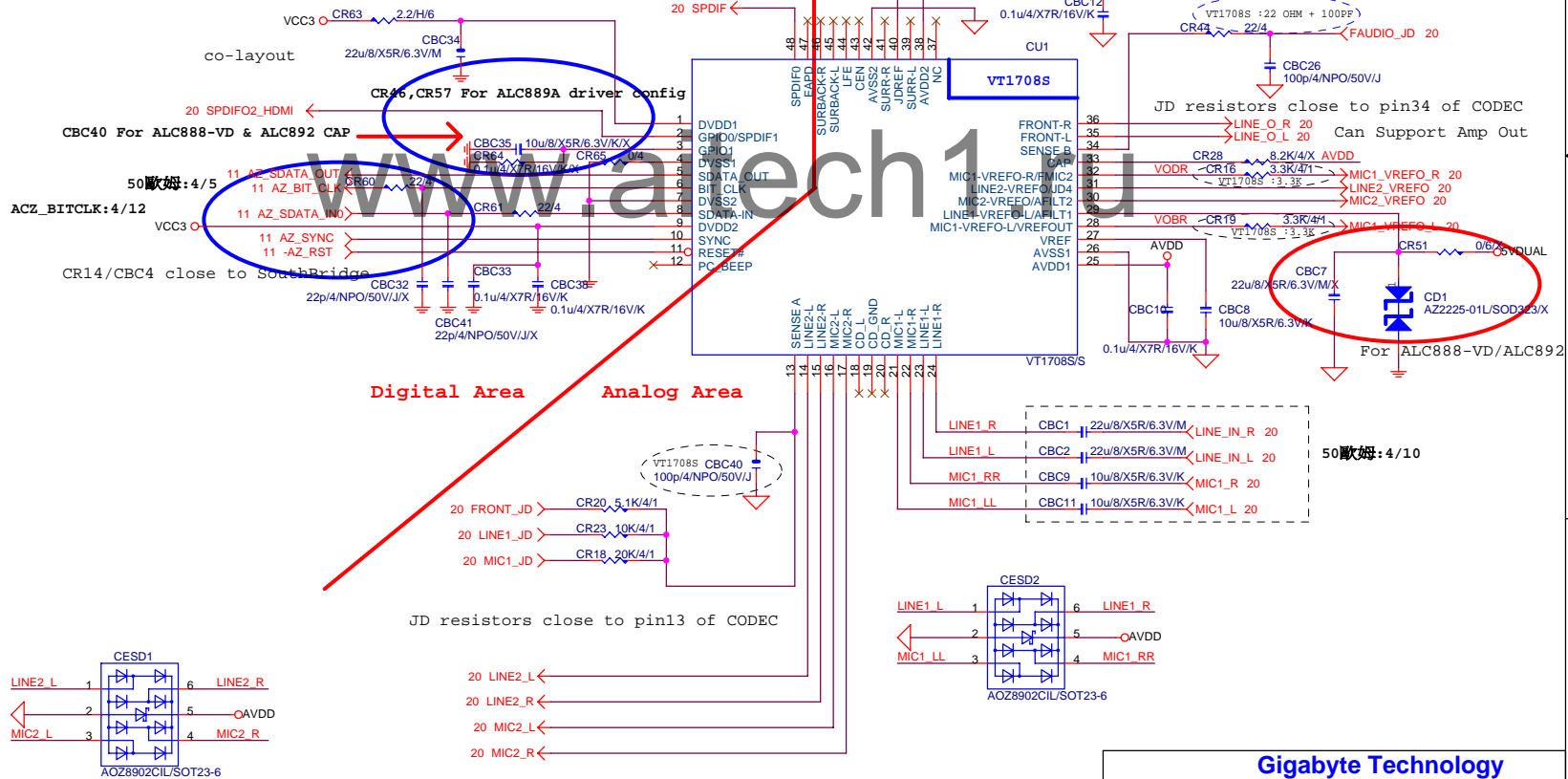
www.aitech1.ru

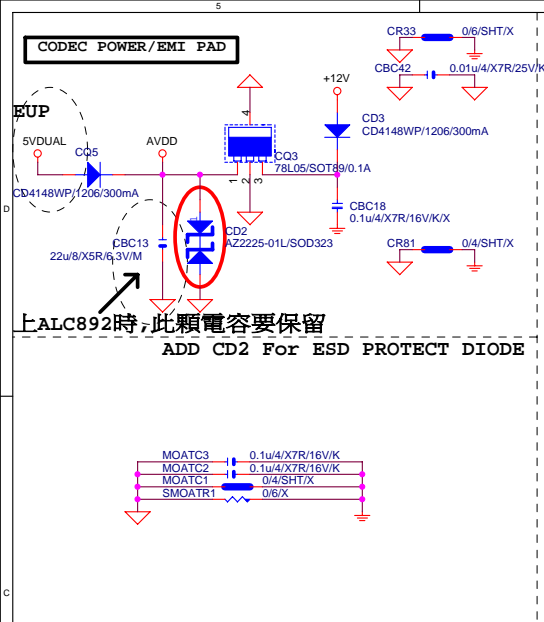
SYSTEM FAN

CPU FAN

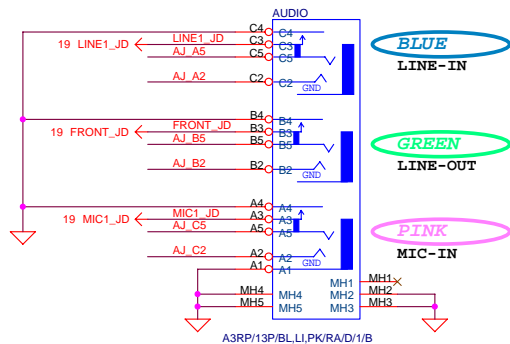
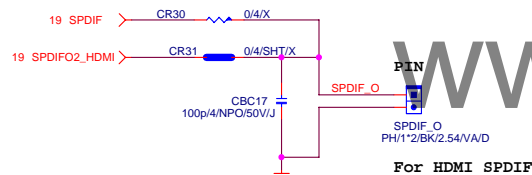


	ALC887-VD2	ALC889	VT1708S	VT1708SCE
CR65	X	O	O	X
CR64	X	X	X	O
CR44/CBC26	47ohm+1nF	47ohm+1nF	22ohm+100P	22ohm+100P
CR34	20K/1%	20K/1%	5.1K/1%	20K/1%
CR31	O	O	O	O
CR30	X	X	X	X
CBC1/CBC2	22uF/X5R	22uF/X5R	22uF/X5R	22uF/X5R
CR20	5.11K/4/1	5.11K/4/1	5.1K/4/1	5.1K/4/1
CBC35	O	X	X	O
CBC39/CBC40	N/A	N/A	100P/4	100P/4
CR6/CR7/CR54/CR58	22K/4	22K/4	10K/4	10K/4
CR5/CR8/CR13/CR11/ CR57/CR53	75 ohm	62 ohm	75 ohm	75 ohm
CR51/CD1/CBC7	O	X	X	O
CD2/CD3/CQ3/CQ5	X	O	O	X
CR1/CR14/CR17/CR22	75 ohm	62 ohm	1K ohm	1K ohm

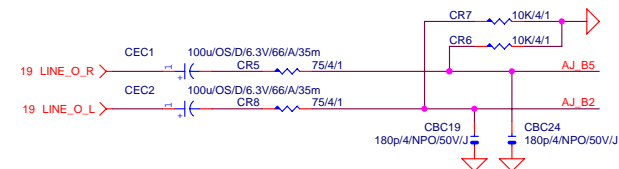




SPDIF_OUT

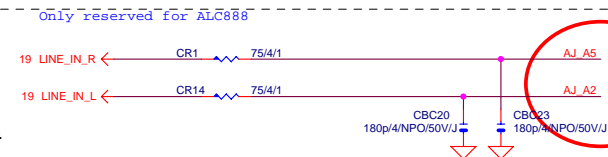


LINE-OUT



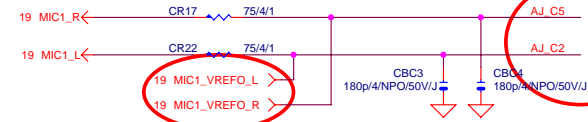
LINE-IN

Verify MIC function
in LINE-in



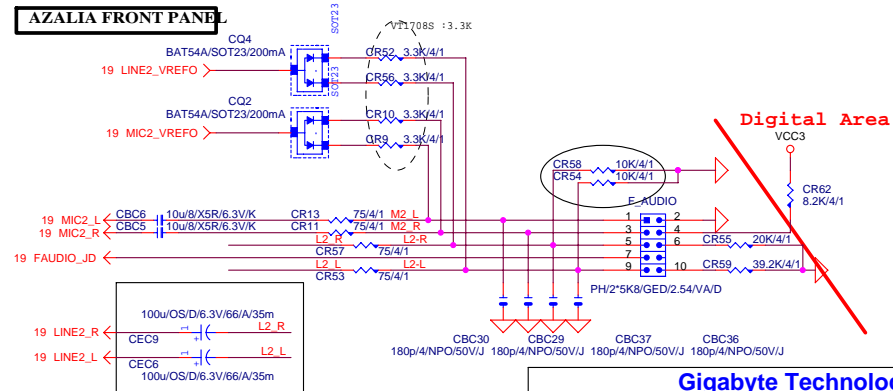
MIC-IN

For 889A/888



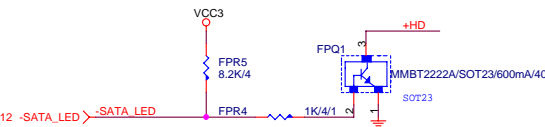
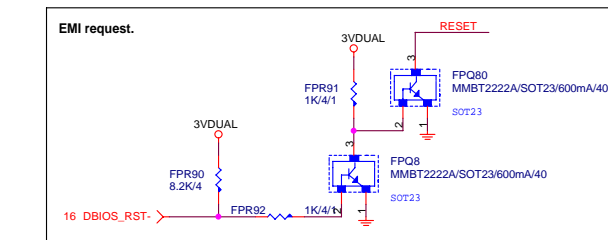
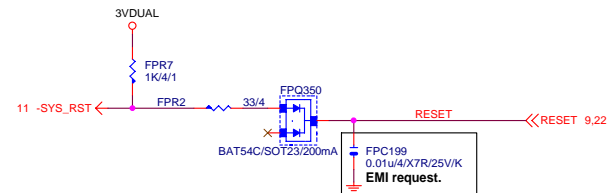
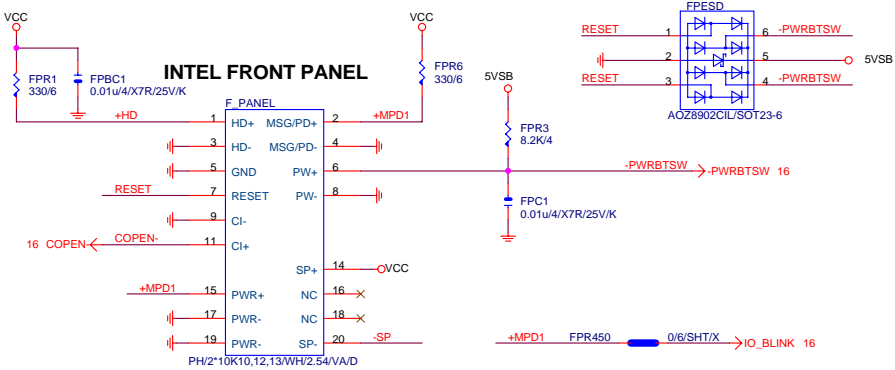
www.aitech1.ru

AZALIA FRONT PANEL

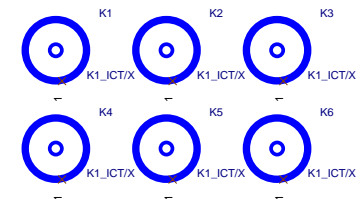
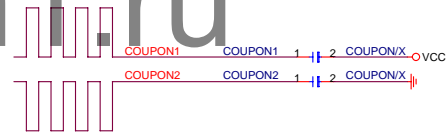


Gigabyte Technology

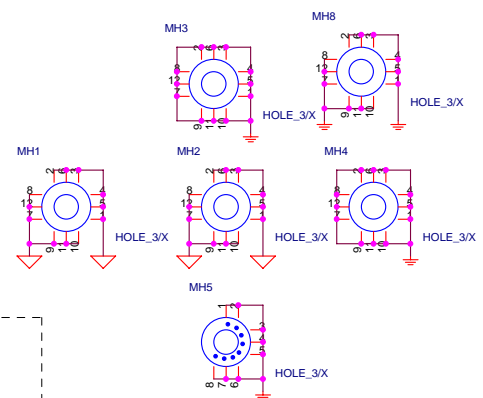
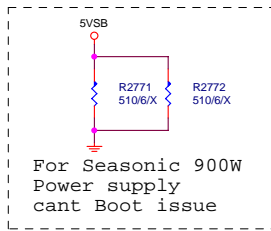
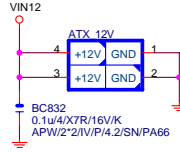
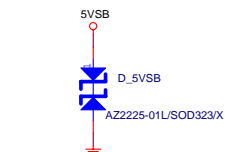
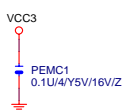
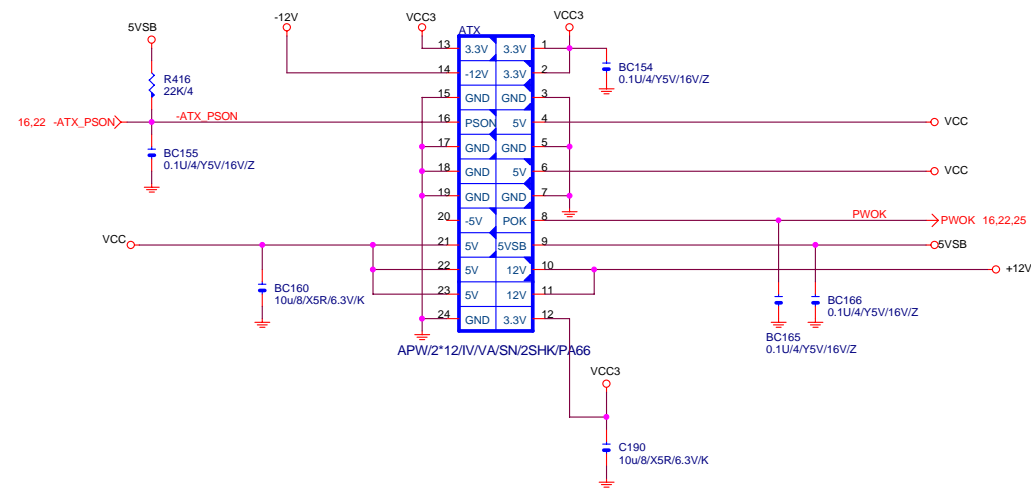
Title			
AUDIO JACK			
Size	Document Number	GA-A55M-DS2	
Custom		Rev 1.01	
Date:	Tuesday, August 23, 2011	Sheet	20 of 27



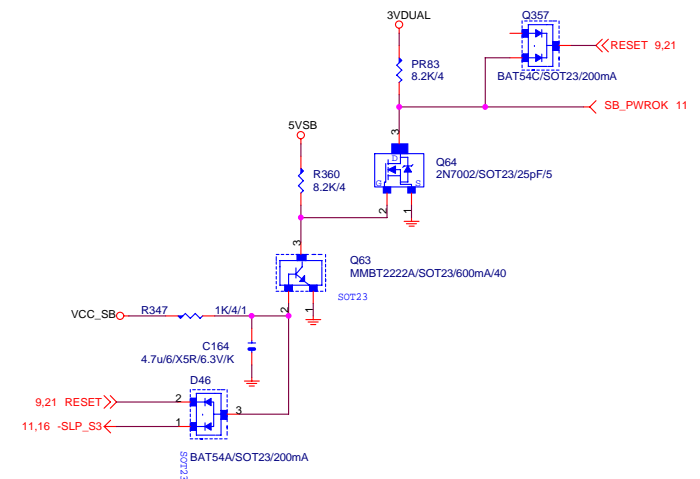
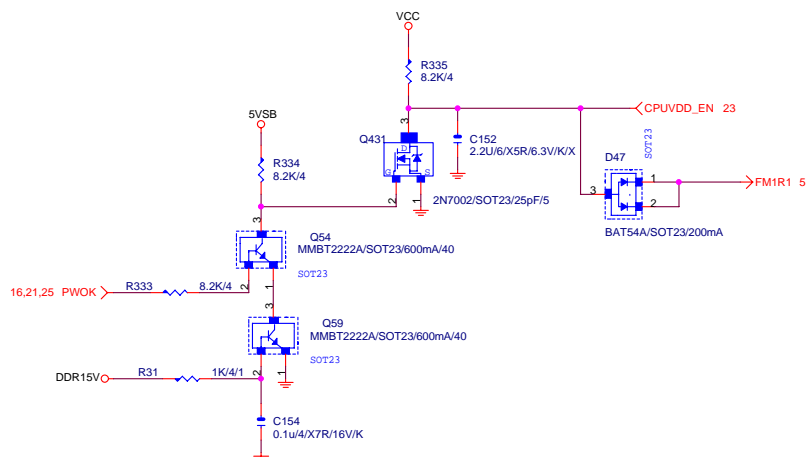
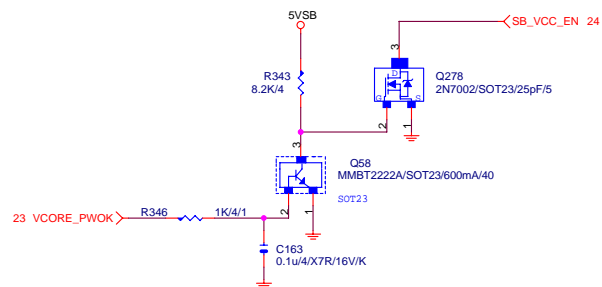
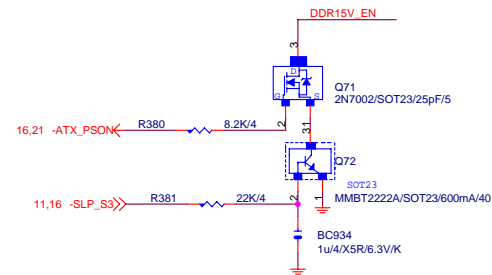
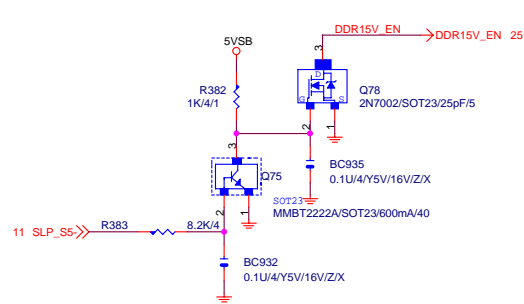
www.aitech1.ru



ATX POWER CONNECTOR



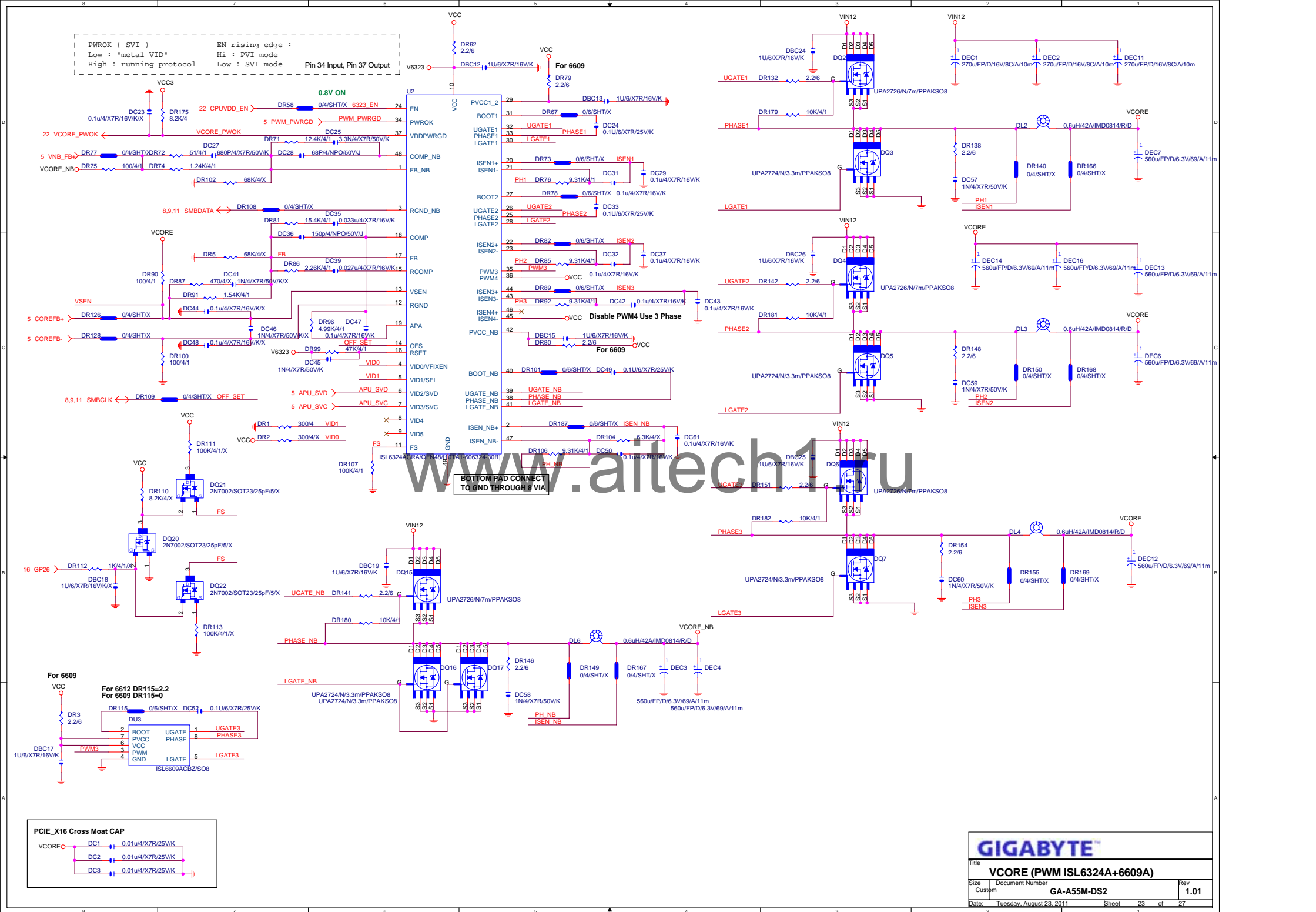
GIGABYTE		
Title	ATX, FRONT PANEL	
Size	Document Number	Rev
Custm	GA-A55M-DS2	1.01
Date:	Tuesday, August 23, 2011	Sheet 21 of 27

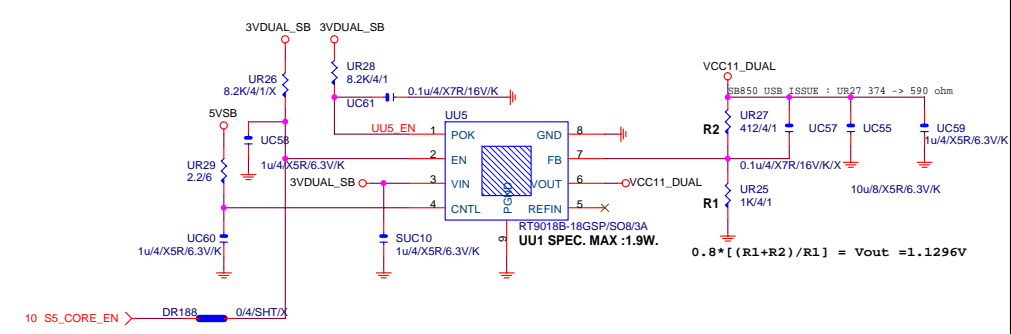
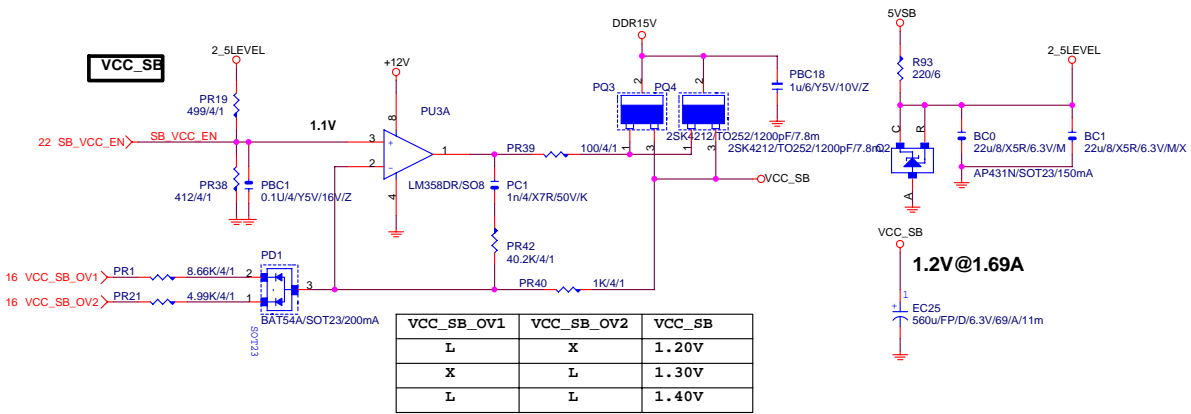


www.aitech1.ru

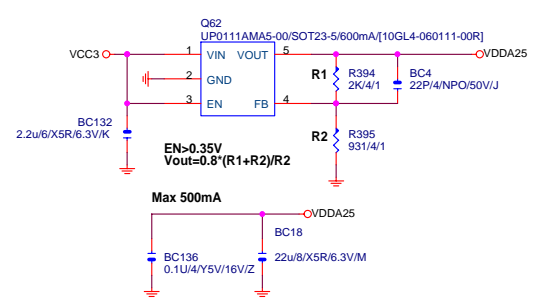
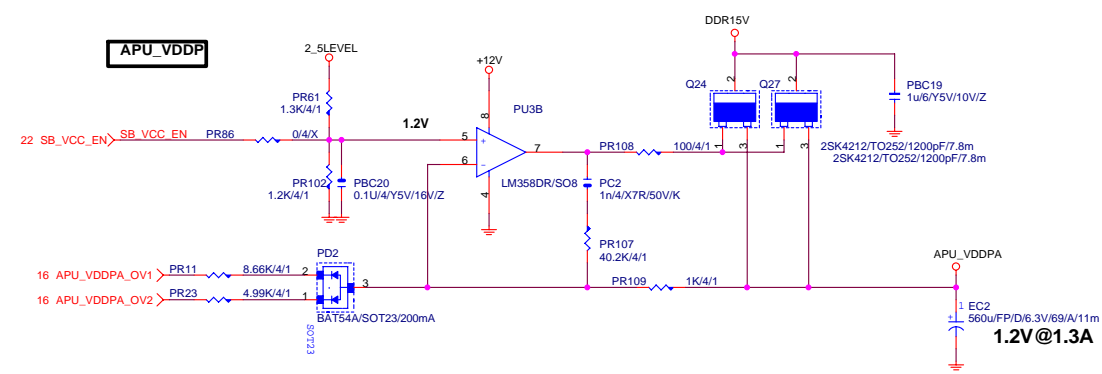
GIGABYTE™

Title		POWER SEQUENCE	
Size	Document Number	Rev	
Custom	GA-A55M-DS2	1.01	
Date:	Tuesday, August 23, 2011	Sheet	22 of 27





www.aitech1.ru



LAN:AR8151/AR8161

